| Ref<br># | Hits | Search Query   | DBs   | Default<br>Operator | Plurals | Time Stamp       |
|----------|------|--|---|---------------------|---------|------------------|
| L1       | 329  | (two-level or multi-level or<br>multilevel) with DRAM  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/07/18 15:00 |
| L2<br>·  | 78   | mask\$3 with ((natural or passive) with transistor)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON .    | 2005/07/18 15:00 |
| L4       | 1    | 1 same 2   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/07/18 14:59 |
| L5       | 4653 | mask\$3 with (channel with transistor)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR .                | ON      | 2005/07/18 15:00 |
| L6       | 1    | 1 same 5   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/07/18 15:00 |
| L7       | 1    | (((two-level or multi-level or<br>multilevel) with DRAM) and<br>(mask\$3 with (channel oe natural<br>or passive)) and implant\$3).clm. | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/07/18 15:02 |